

1/4

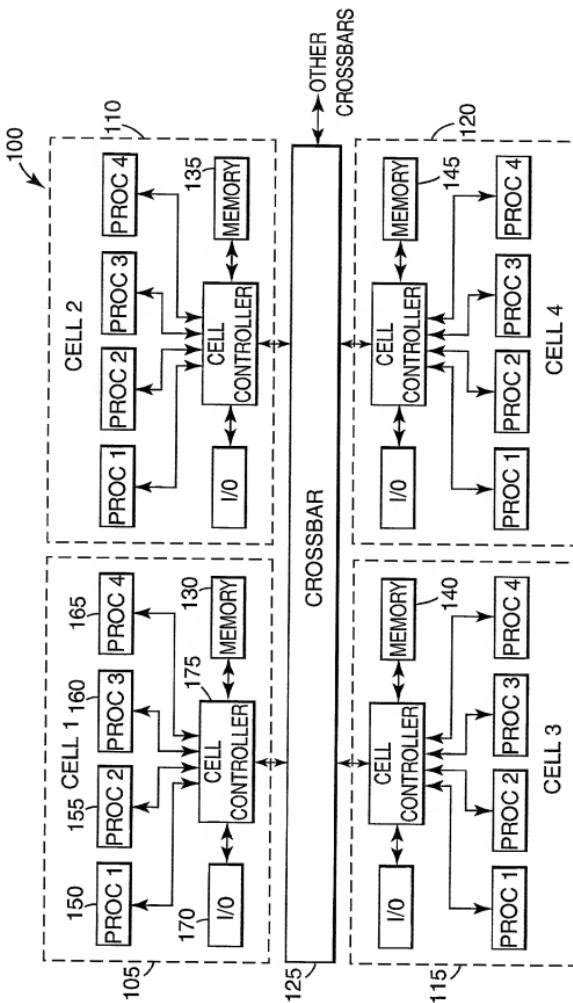


Fig. 1

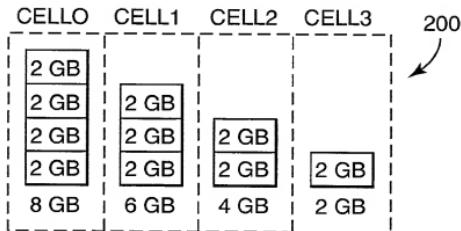


Fig. 2

Table 3 shows the mapping of memory addresses to cell indices. The columns are labeled 000, 001, 010, 011, 100, 101, 110, and 111. The rows are labeled 000, 001, 010, 011, 100, 101, 110, and 111. The data values are as follows:

	000	001	010	011	100	101	110	111
000	0	1	2	3	0	1	2	3
001	1	2	3	0	1	2	3	0
010	2	3	0	1	4	5	6	7
011	3	0	1	2	5	6	7	0
100	0	1	2	3	4	5	6	7
101	1	2	0	1	0	1	2	0
110	2	0	1	2	0	1	0	1
111	0	1	2	3	1	2	0	2

Fig. 3

Table 4 shows a different mapping of memory addresses to cell indices. The columns are labeled 000, 001, 010, 011, 100, 101, 110, and 111. The rows are labeled 000, 001, 010, 011, 100, 101, 110, and 111. The data values are as follows:

	000	001	010	011	100	101	110	111
000	0	1	2	0	1	2	0	1
001	1	2	0	1	2	0	1	2
010	2	0	1	2	0	1	2	0
011	0	1	2	0	1	2	0	1
100	1	2	0	1	2	0	1	2
101	2	0	1	2	0	1	2	0
110	0	1	2	0	1	2	0	1
111	1	2	0	1	2	0	1	2

Fig. 4

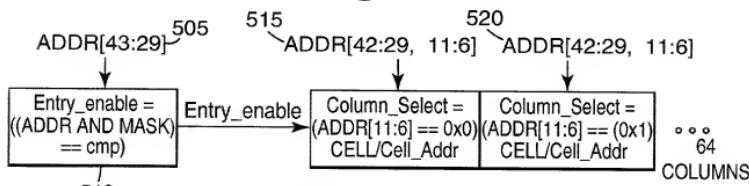


Fig. 5

3/4

0000	605
0001	
0010	
0011	
0100	610
0101	
0110	
0111	
1000	615
1001	
1010	
1011	
1100	620
1101	
1110	
1111	

Fig. 6

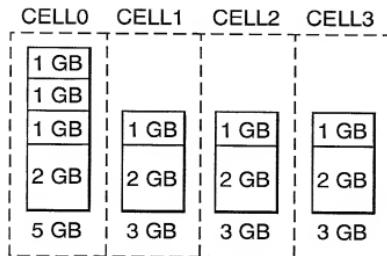


Fig. 7

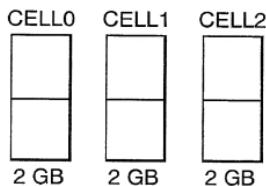


Fig. 8

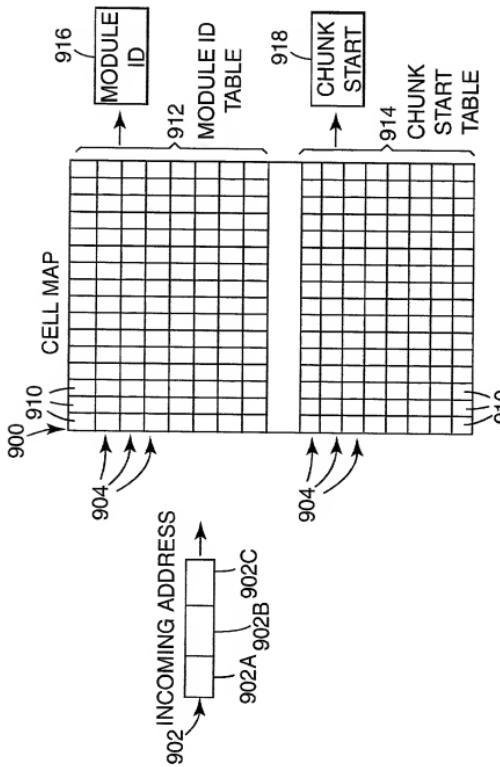


Fig. 9